/\*

u8g\_com\_arduino\_st7920\_spi.c

Universal 8bit Graphics Library

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A special SPI interface for ST7920 controller

Update for ATOMIC operation done (01 Jun 2013)

U8G\_ATOMIC\_OR(ptr, val)

U8G\_ATOMIC\_AND(ptr, val)

U8G\_ATOMIC\_START();

U8G\_ATOMIC\_END();

\*/

#include "u8g.h"

#if defined(ARDUINO)

#if ARDUINO < 100

#include <WProgram.h>

#include "wiring\_private.h"

#include "pins\_arduino.h"

#else

#include <Arduino.h>

#include "wiring\_private.h"

#endif

#if defined(\_\_AVR\_\_)

static uint8\_t u8g\_bitData, u8g\_bitNotData;

static uint8\_t u8g\_bitClock, u8g\_bitNotClock;

static volatile uint8\_t \*u8g\_outData;

static volatile uint8\_t \*u8g\_outClock;

static void u8g\_com\_arduino\_init\_shift\_out(uint8\_t dataPin, uint8\_t clockPin)

{

u8g\_outData = portOutputRegister(digitalPinToPort(dataPin));

u8g\_outClock = portOutputRegister(digitalPinToPort(clockPin));

u8g\_bitData = digitalPinToBitMask(dataPin);

u8g\_bitClock = digitalPinToBitMask(clockPin);

u8g\_bitNotClock = u8g\_bitClock;

u8g\_bitNotClock ^= 0x0ff;

u8g\_bitNotData = u8g\_bitData;

u8g\_bitNotData ^= 0x0ff;

}

static void u8g\_com\_arduino\_do\_shift\_out\_msb\_first(uint8\_t val) U8G\_NOINLINE;

static void u8g\_com\_arduino\_do\_shift\_out\_msb\_first(uint8\_t val)

{

uint8\_t cnt = 8;

uint8\_t bitData = u8g\_bitData;

uint8\_t bitNotData = u8g\_bitNotData;

uint8\_t bitClock = u8g\_bitClock;

uint8\_t bitNotClock = u8g\_bitNotClock;

volatile uint8\_t \*outData = u8g\_outData;

volatile uint8\_t \*outClock = u8g\_outClock;

U8G\_ATOMIC\_START();

bitData |= \*outData;

bitNotData &= \*outData;

do

{

if ( val & 128 )

\*outData = bitData;

else

\*outData = bitNotData;

/\*

\*outClock |= bitClock;

val <<= 1;

cnt--;

\*outClock &= bitNotClock;

\*/

val <<= 1;

\*outClock &= bitNotClock;

cnt--;

// removed micro delays, because AVRs are too slow and the delay is not required

//u8g\_MicroDelay();

\*outClock |= bitClock;

//u8g\_MicroDelay();

} while( cnt != 0 );

U8G\_ATOMIC\_END();

}

#elif defined(\_\_18CXX) || defined(\_\_PIC32MX)

uint16\_t dog\_bitData, dog\_bitNotData;

uint16\_t dog\_bitClock, dog\_bitNotClock;

volatile uint32\_t \*dog\_outData;

volatile uint32\_t \*dog\_outClock;

volatile uint32\_t dog\_pic32\_spi\_tmp;

static void u8g\_com\_arduino\_init\_shift\_out(uint8\_t dataPin, uint8\_t clockPin)

{

dog\_outData = portOutputRegister(digitalPinToPort(dataPin));

dog\_outClock = portOutputRegister(digitalPinToPort(clockPin));

dog\_bitData = digitalPinToBitMask(dataPin);

dog\_bitClock = digitalPinToBitMask(clockPin);

dog\_bitNotClock = dog\_bitClock;

dog\_bitNotClock ^= 0x0ffff;

dog\_bitNotData = dog\_bitData;

dog\_bitNotData ^= 0x0ffff;

}

static void u8g\_com\_arduino\_do\_shift\_out\_msb\_first(uint8\_t val)

{

uint8\_t cnt = 8;

U8G\_ATOMIC\_START();

do

{

if ( val & 128 )

\*dog\_outData |= dog\_bitData;

else

\*dog\_outData &= dog\_bitNotData;

val <<= 1;

//u8g\_MicroDelay();

//\*dog\_outClock |= dog\_bitClock;

\*dog\_outClock &= dog\_bitNotClock;

cnt--;

u8g\_MicroDelay();

//\*dog\_outClock &= dog\_bitNotClock;

\*dog\_outClock |= dog\_bitClock;

u8g\_MicroDelay();

} while( cnt != 0 );

U8G\_ATOMIC\_END();

}

#else

/\* default interface, Arduino DUE (\_\_arm\_\_) \*/

uint8\_t u8g\_data\_pin;

uint8\_t u8g\_clock\_pin;

static void u8g\_com\_arduino\_init\_shift\_out(uint8\_t dataPin, uint8\_t clockPin)

{

u8g\_data\_pin = dataPin;

u8g\_clock\_pin = clockPin;

}

static void u8g\_com\_arduino\_do\_shift\_out\_msb\_first(uint8\_t val)

{

uint8\_t cnt = 8;

do

{

if ( val & 128 )

digitalWrite(u8g\_data\_pin, HIGH);

else

digitalWrite(u8g\_data\_pin, LOW);

val <<= 1;

//u8g\_MicroDelay();

digitalWrite(u8g\_clock\_pin, LOW);

cnt--;

u8g\_MicroDelay();

digitalWrite(u8g\_clock\_pin, HIGH);

u8g\_MicroDelay();

} while( cnt != 0 );

}

#endif

static void u8g\_com\_arduino\_st7920\_write\_byte\_seq(uint8\_t rs, uint8\_t \*ptr, uint8\_t len)

{

uint8\_t i;

if ( rs == 0 )

{

/\* command \*/

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(0x0f8);

}

else if ( rs == 1 )

{

/\* data \*/

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(0x0fa);

}

while( len > 0 )

{

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(\*ptr & 0x0f0);

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(\*ptr << 4);

ptr++;

len--;

u8g\_10MicroDelay();

}

for( i = 0; i < 4; i++ )

u8g\_10MicroDelay();

}

static void u8g\_com\_arduino\_st7920\_write\_byte(uint8\_t rs, uint8\_t val)

{

uint8\_t i;

if ( rs == 0 )

{

/\* command \*/

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(0x0f8);

}

else if ( rs == 1 )

{

/\* data \*/

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(0x0fa);

}

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(val & 0x0f0);

u8g\_com\_arduino\_do\_shift\_out\_msb\_first(val << 4);

for( i = 0; i < 4; i++ )

u8g\_10MicroDelay();

}

uint8\_t u8g\_com\_arduino\_st7920\_spi\_fn(u8g\_t \*u8g, uint8\_t msg, uint8\_t arg\_val, void \*arg\_ptr)

{

switch(msg)

{

case U8G\_COM\_MSG\_INIT:

u8g\_com\_arduino\_assign\_pin\_output\_high(u8g);

u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_CS, LOW);

// u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_SCK, LOW);

u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_SCK, HIGH);

u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_MOSI, LOW);

u8g\_com\_arduino\_init\_shift\_out(u8g->pin\_list[U8G\_PI\_MOSI], u8g->pin\_list[U8G\_PI\_SCK]);

u8g->pin\_list[U8G\_PI\_A0\_STATE] = 0; /\* inital RS state: command mode \*/

break;

case U8G\_COM\_MSG\_STOP:

break;

case U8G\_COM\_MSG\_RESET:

if ( u8g->pin\_list[U8G\_PI\_RESET] != U8G\_PIN\_NONE )

u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_RESET, arg\_val);

break;

case U8G\_COM\_MSG\_CHIP\_SELECT:

if ( arg\_val == 0 )

{

/\* disable, note: the st7920 has an active high chip select \*/

u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_CS, LOW);

}

else

{

/\* enable \*/

//u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_SCK, HIGH);

u8g\_com\_arduino\_digital\_write(u8g, U8G\_PI\_CS, HIGH);

/\* 28 Dec 2013 reassign pins, fixes issue with more than one display \*/

/\* issue 227 \*/

u8g\_com\_arduino\_init\_shift\_out(u8g->pin\_list[U8G\_PI\_MOSI], u8g->pin\_list[U8G\_PI\_SCK]);

}

break;

case U8G\_COM\_MSG\_WRITE\_BYTE:

u8g\_com\_arduino\_st7920\_write\_byte( u8g->pin\_list[U8G\_PI\_A0\_STATE], arg\_val);

//u8g->pin\_list[U8G\_PI\_A0\_STATE] = 2;

//u8g\_arduino\_sw\_spi\_shift\_out(u8g->pin\_list[U8G\_PI\_MOSI], u8g->pin\_list[U8G\_PI\_SCK], arg\_val);

break;

case U8G\_COM\_MSG\_WRITE\_SEQ:

u8g\_com\_arduino\_st7920\_write\_byte\_seq(u8g->pin\_list[U8G\_PI\_A0\_STATE], (uint8\_t \*)arg\_ptr, arg\_val);

break;

case U8G\_COM\_MSG\_WRITE\_SEQ\_P:

{

register uint8\_t \*ptr = arg\_ptr;

while( arg\_val > 0 )

{

u8g\_com\_arduino\_st7920\_write\_byte(u8g->pin\_list[U8G\_PI\_A0\_STATE], u8g\_pgm\_read(ptr) );

//u8g->pin\_list[U8G\_PI\_A0\_STATE] = 2;

ptr++;

arg\_val--;

}

}

break;

case U8G\_COM\_MSG\_ADDRESS: /\* define cmd (arg\_val = 0) or data mode (arg\_val = 1) \*/

u8g->pin\_list[U8G\_PI\_A0\_STATE] = arg\_val;

break;

}

return 1;

}

#else /\* ARDUINO \*/

uint8\_t u8g\_com\_arduino\_st7920\_spi\_fn(u8g\_t \*u8g, uint8\_t msg, uint8\_t arg\_val, void \*arg\_ptr)

{

return 1;

}

#endif /\* ARDUINO \*/